

"Method And System For Implementing A
Reduced Complexity Dual Rate Echo Canceller"
Applicants: Bijit Hadler, et al.
Application No. 10/020,134
Attorney Docket No. 56162.000358

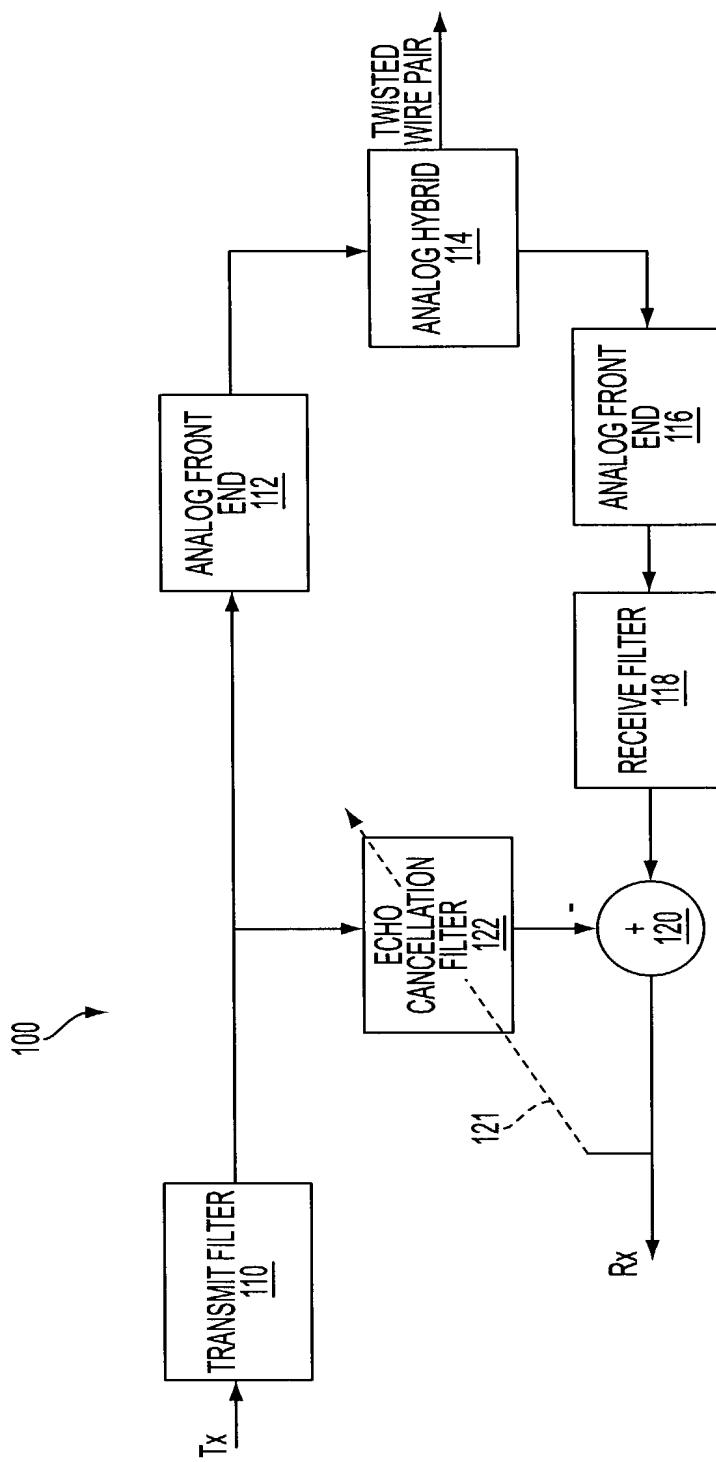


FIG. 1A

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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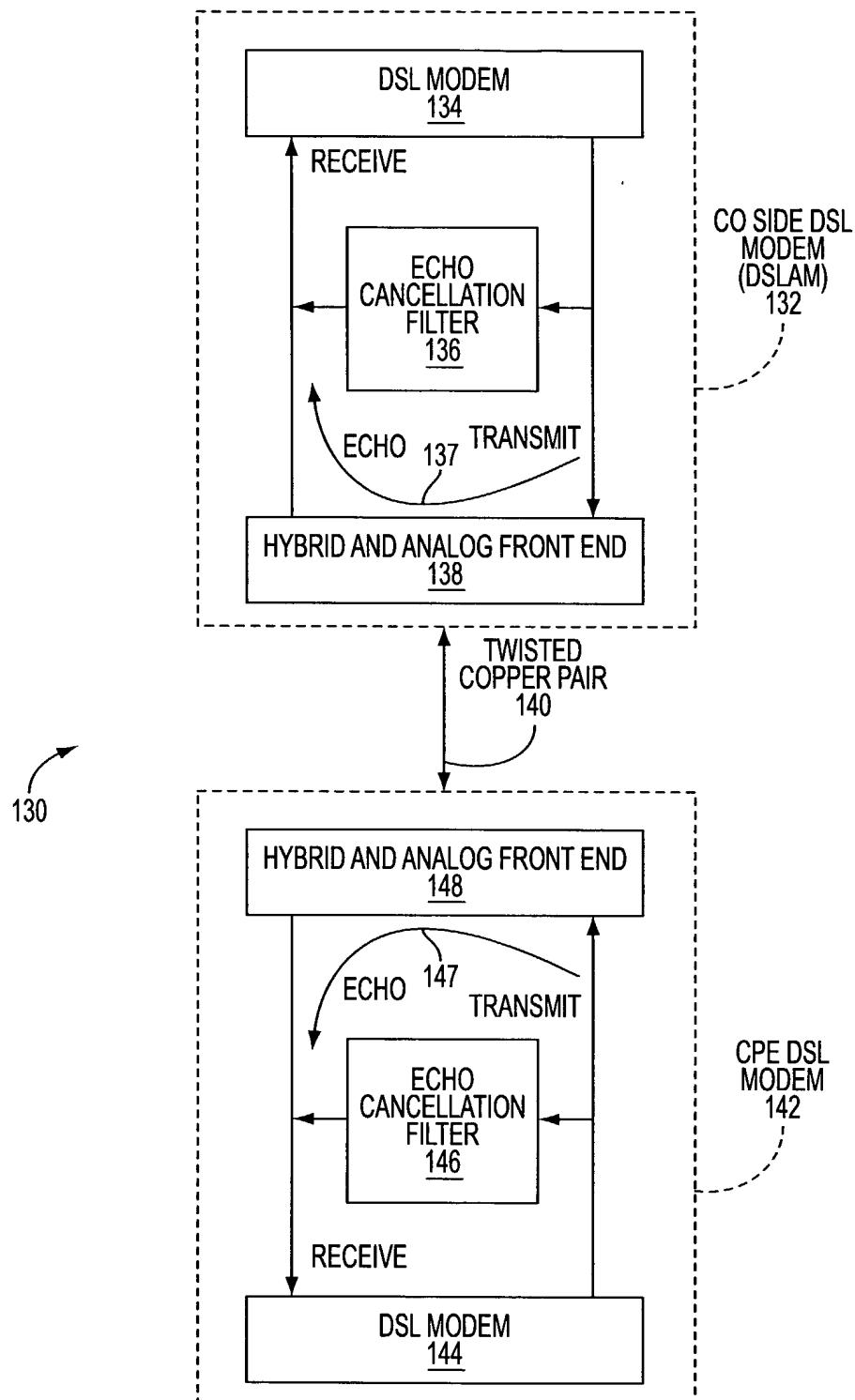


FIG. 1B

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"
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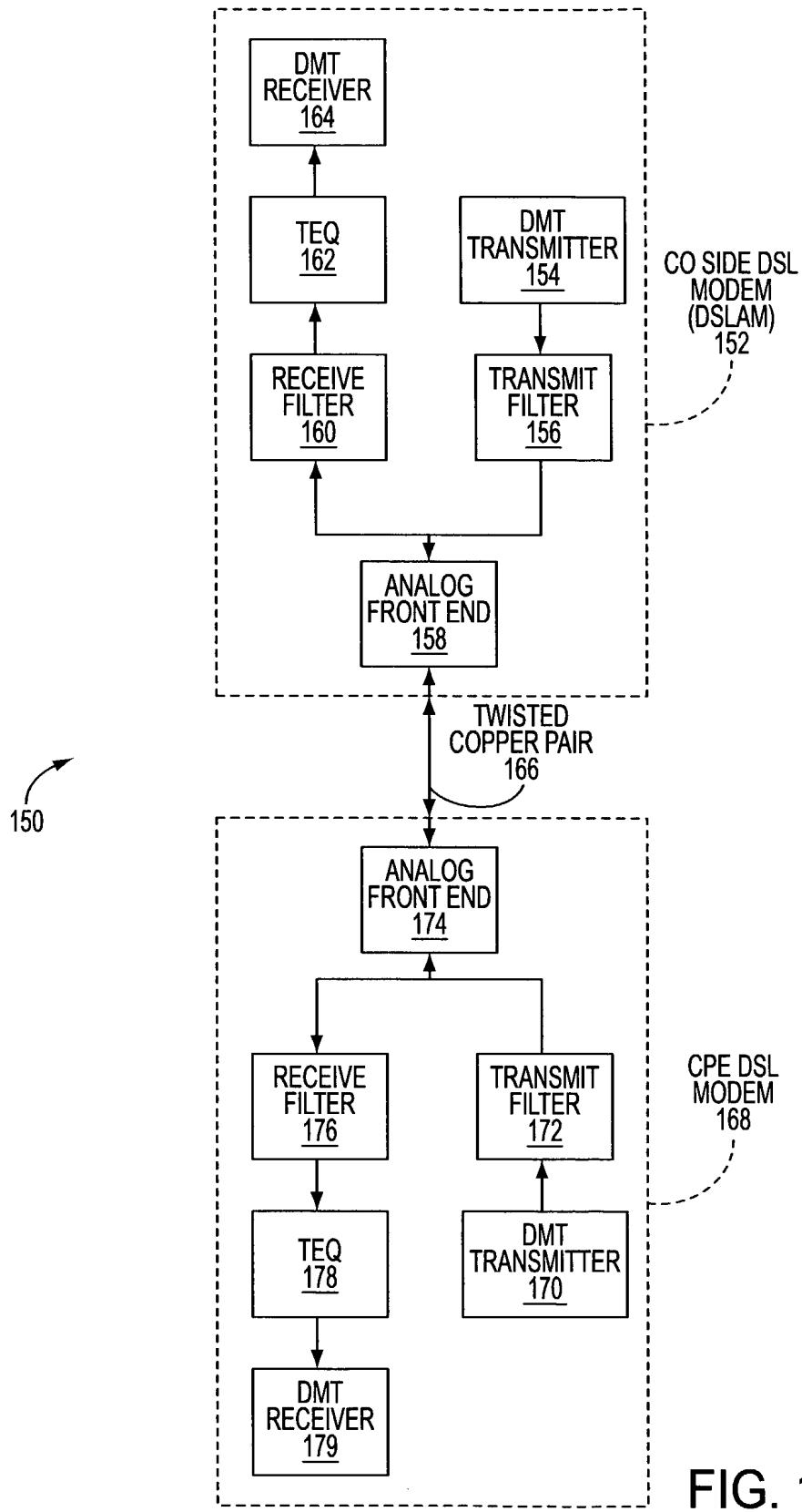


FIG. 1C

"Method And System For Implementing A
Reduced Complexity Dual Rate Echo Cancell
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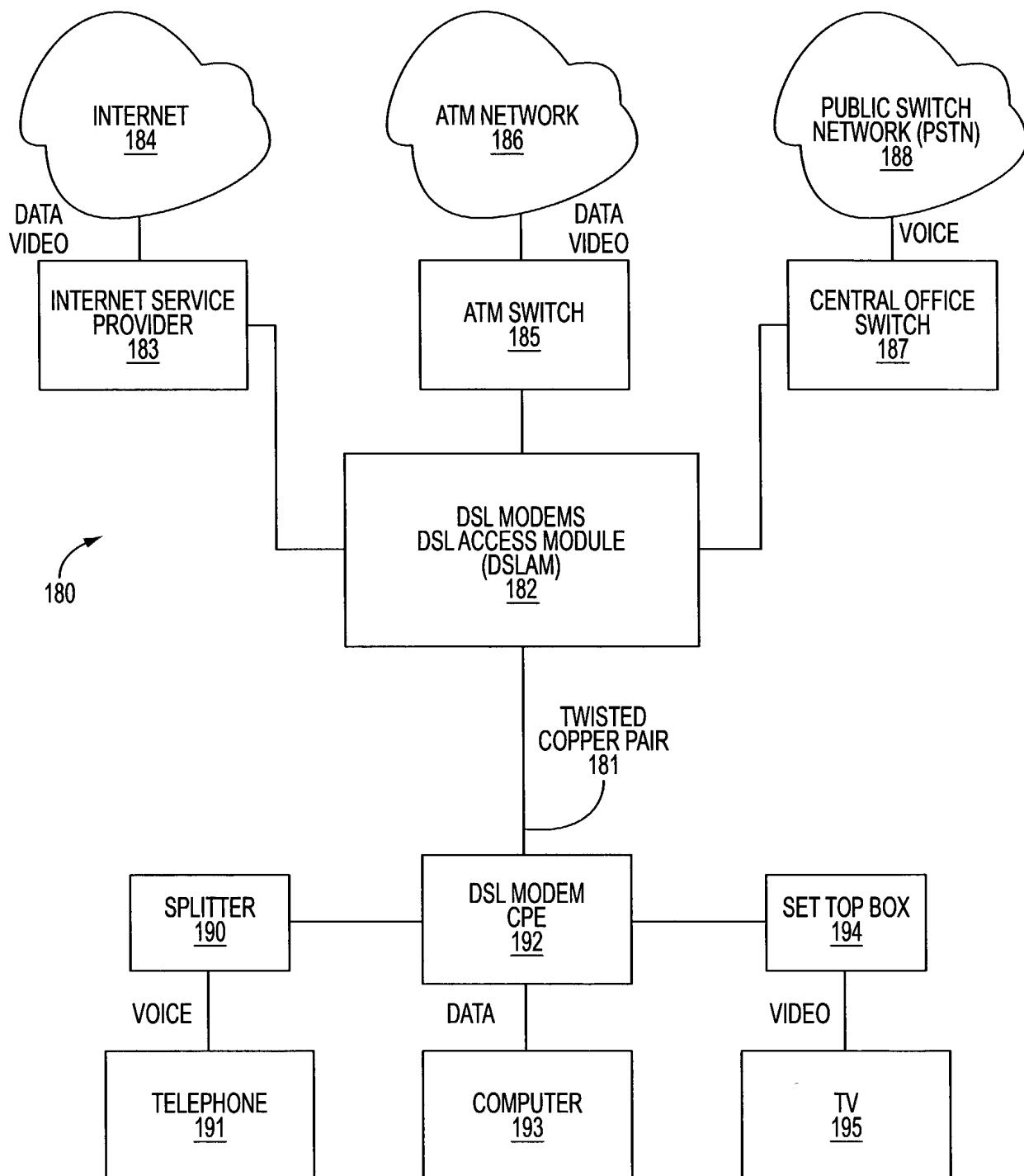


FIG. 1D

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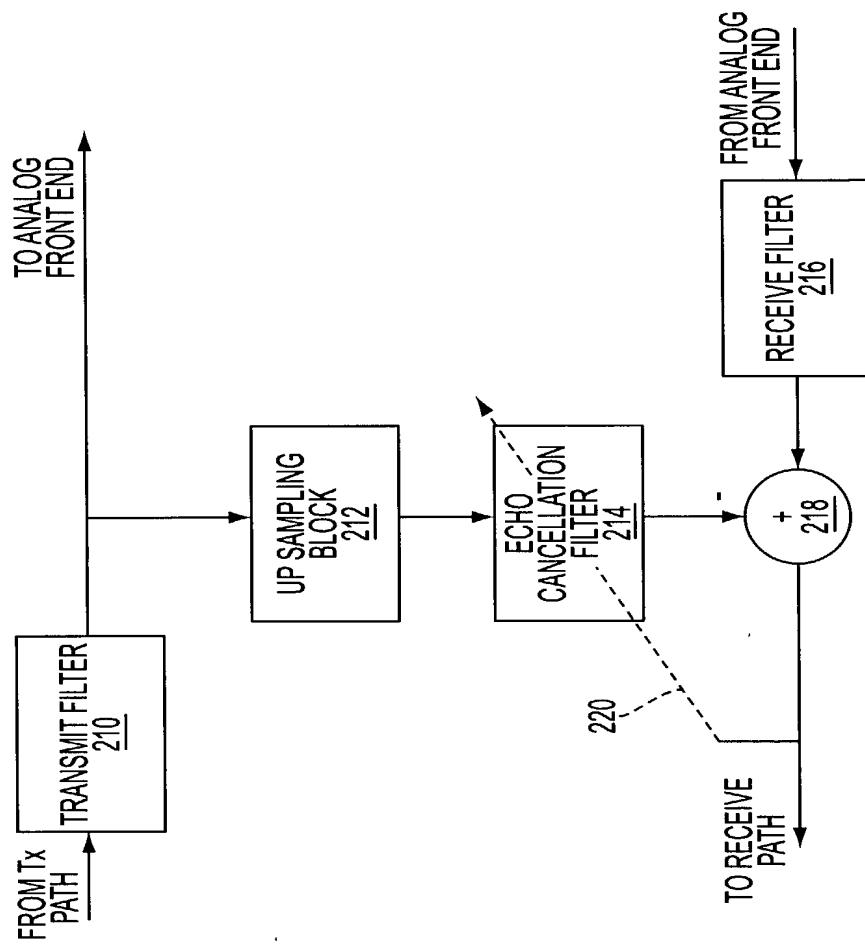


FIG. 2

**"Method And System For Implementing A
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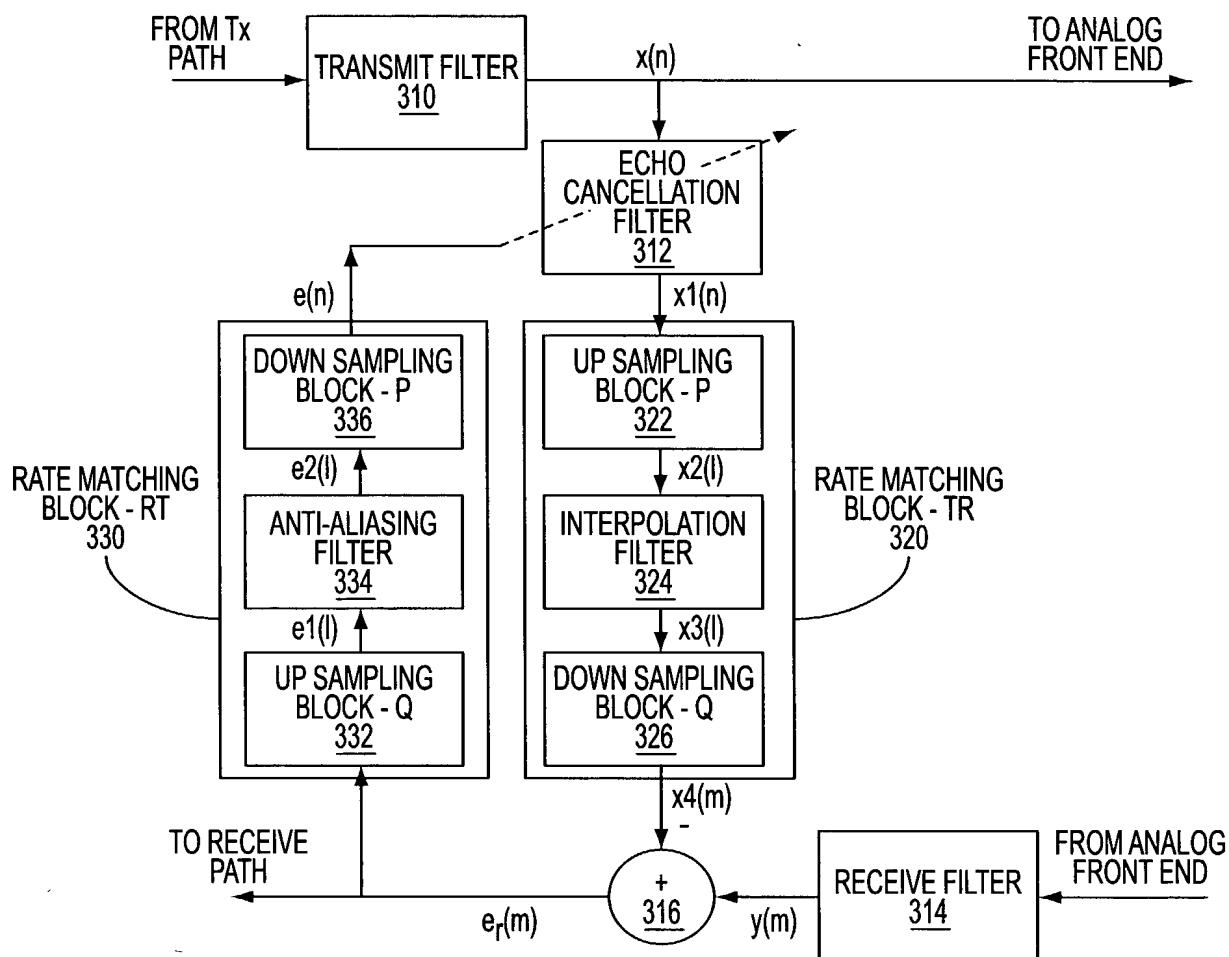


FIG. 3

"Method And System For Implementing A
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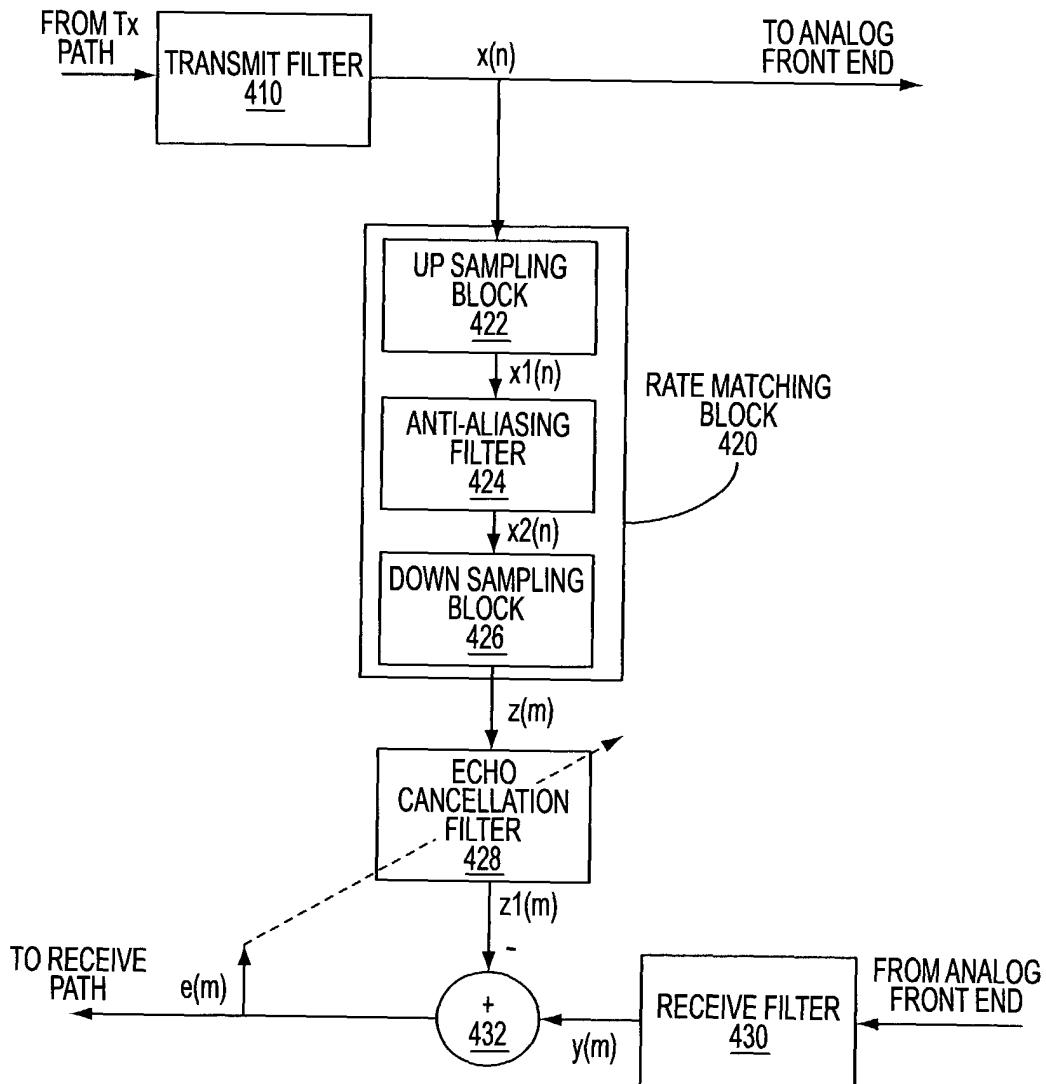


FIG. 4

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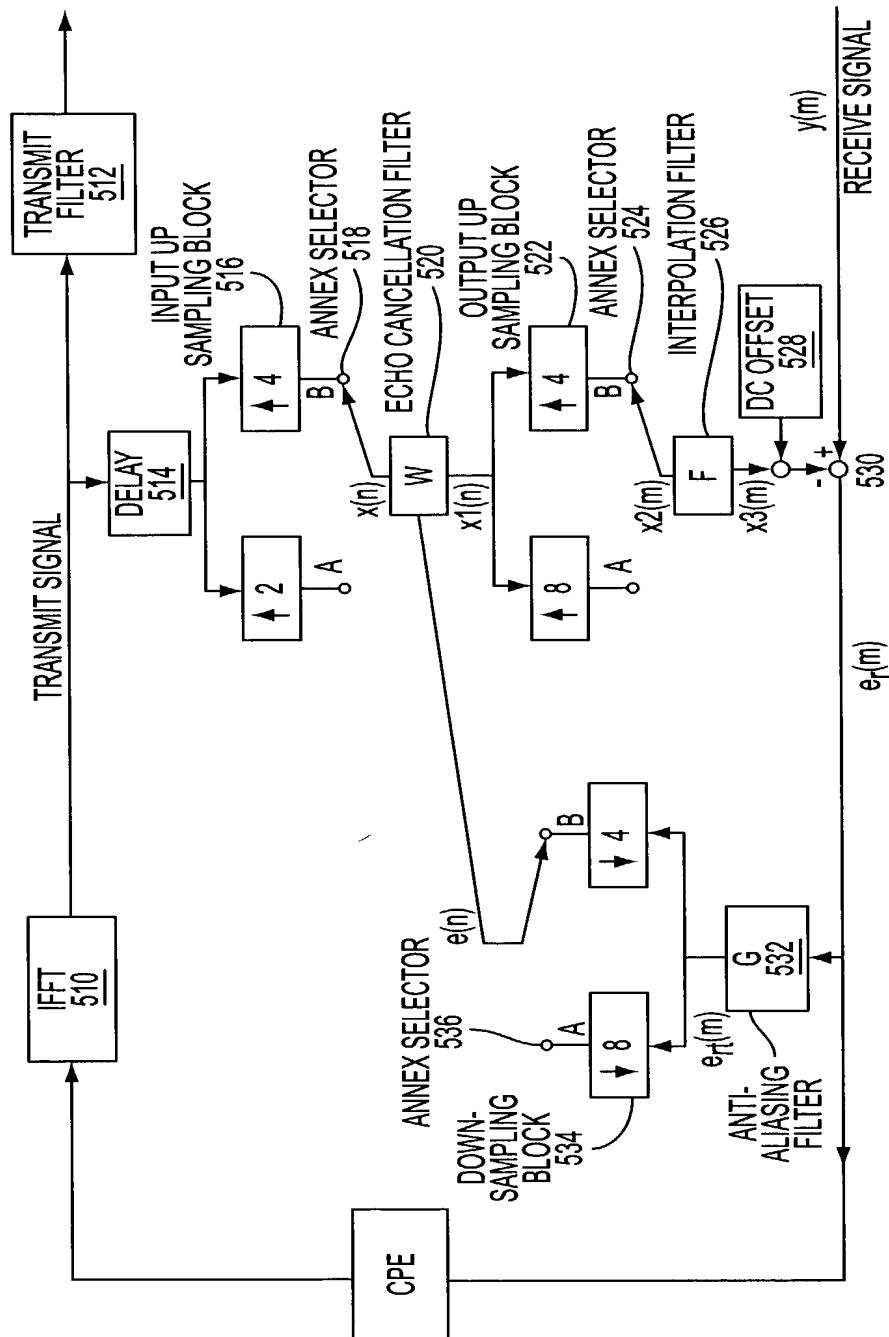


FIG. 5

**"Method And System For Implementing A
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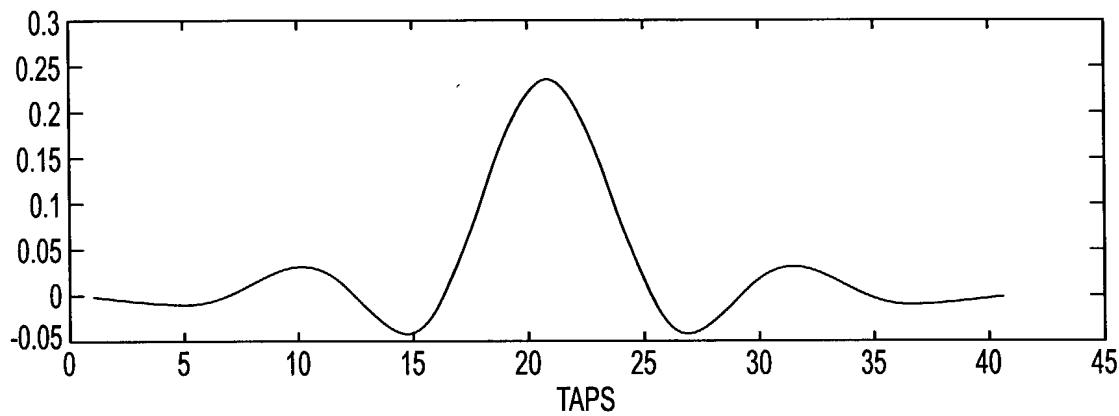


FIG. 6

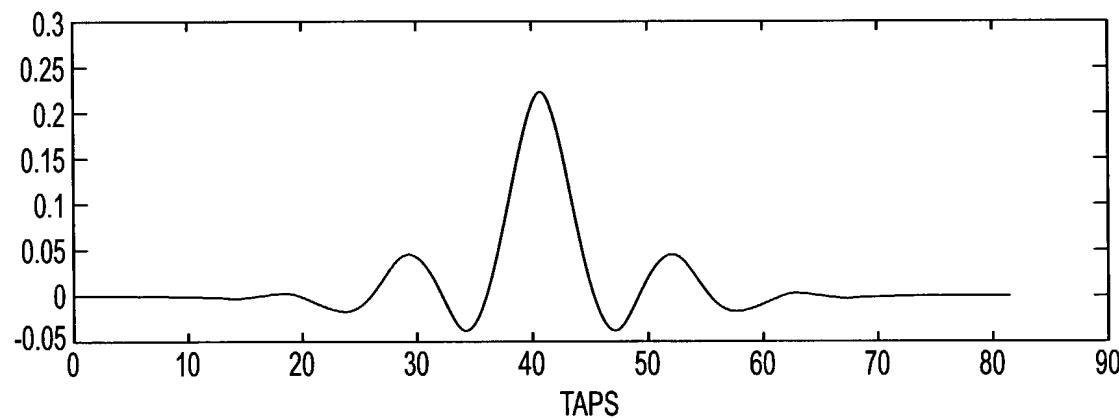


FIG. 7

"Method And System For Implementing A
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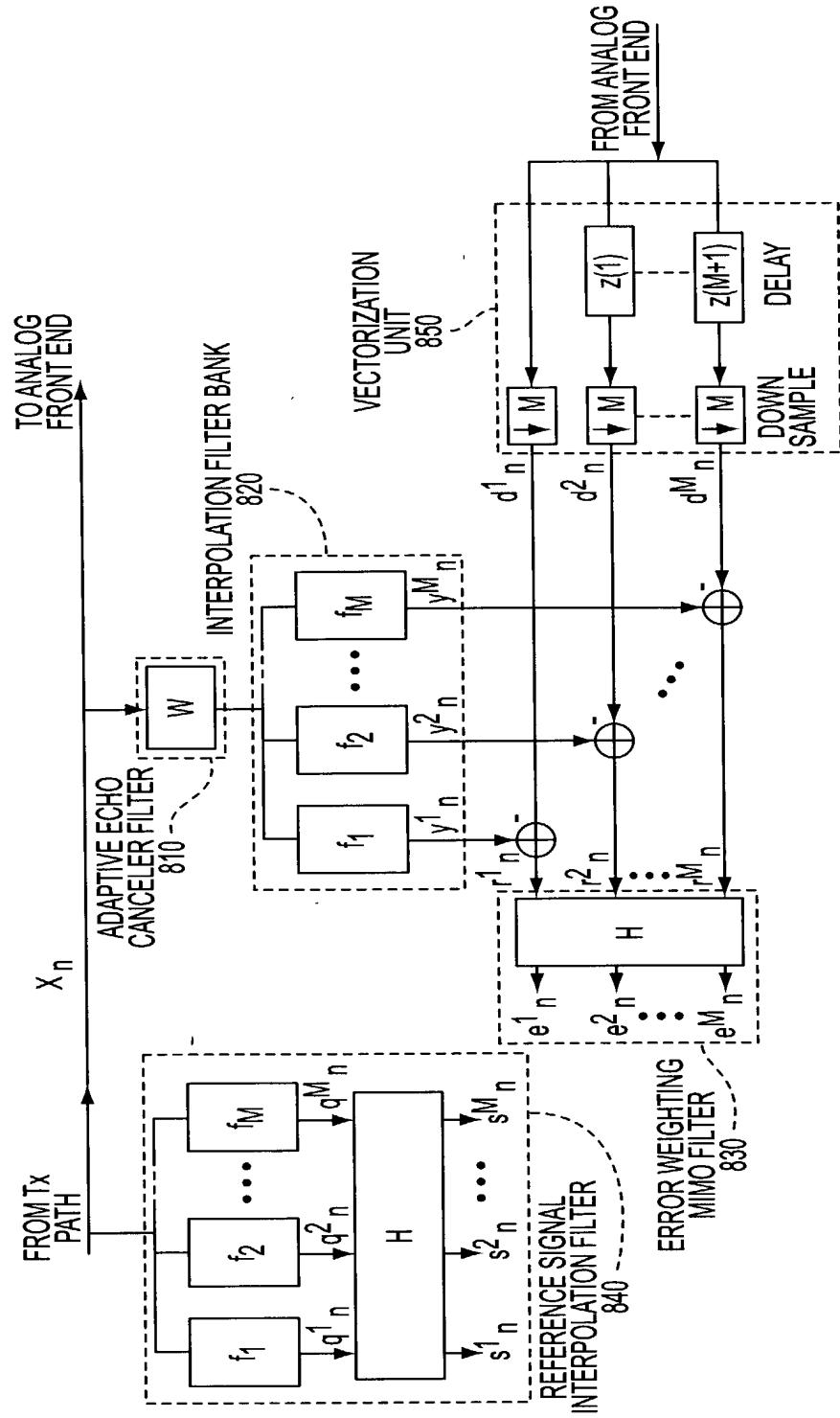


FIG. 8

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"
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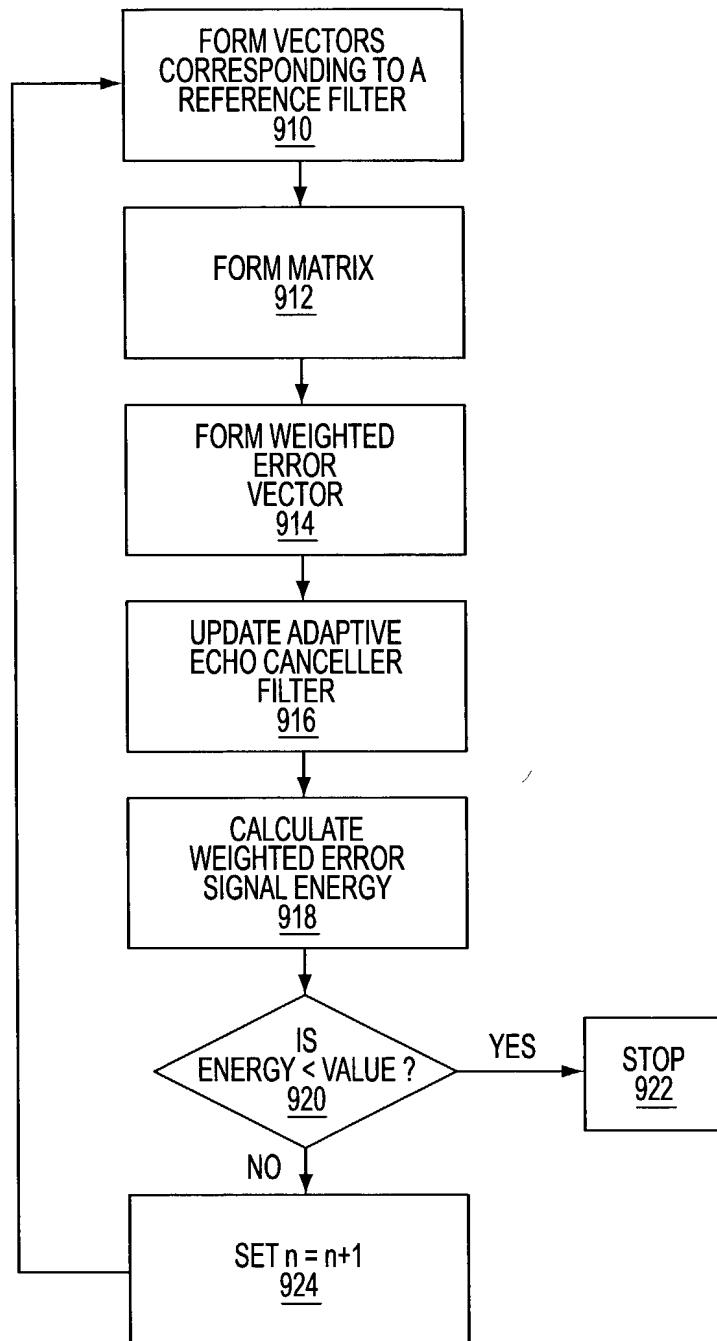


FIG. 9

"Method And System For Implementing A
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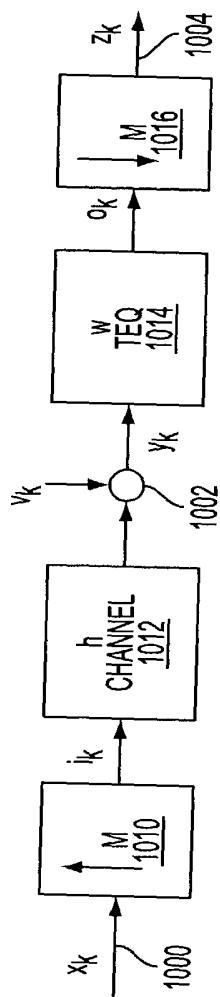


FIG. 10A

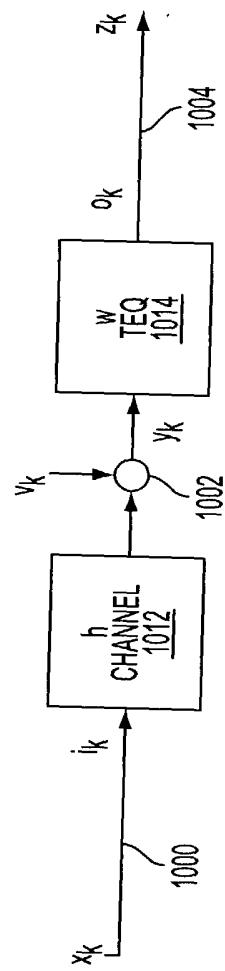


FIG. 10B

“Method And System For Implementing A
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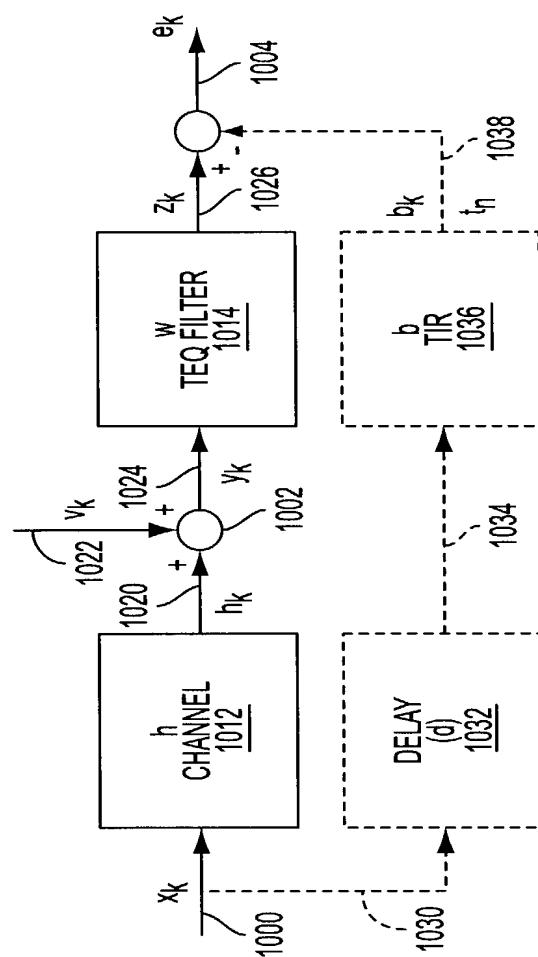


FIG. 10C

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"
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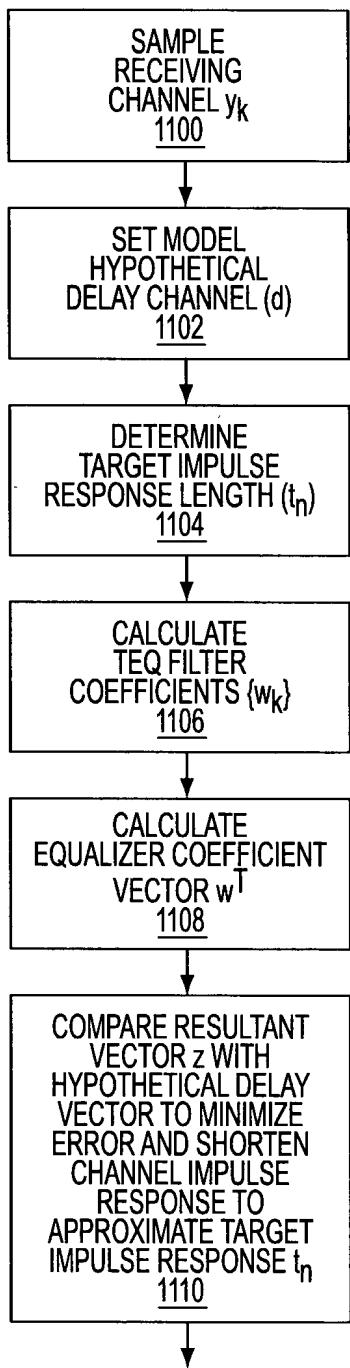


FIG. 11

"Method And System For Implementing A
Reduced Complexity Dual Rate Echo Cancellor"
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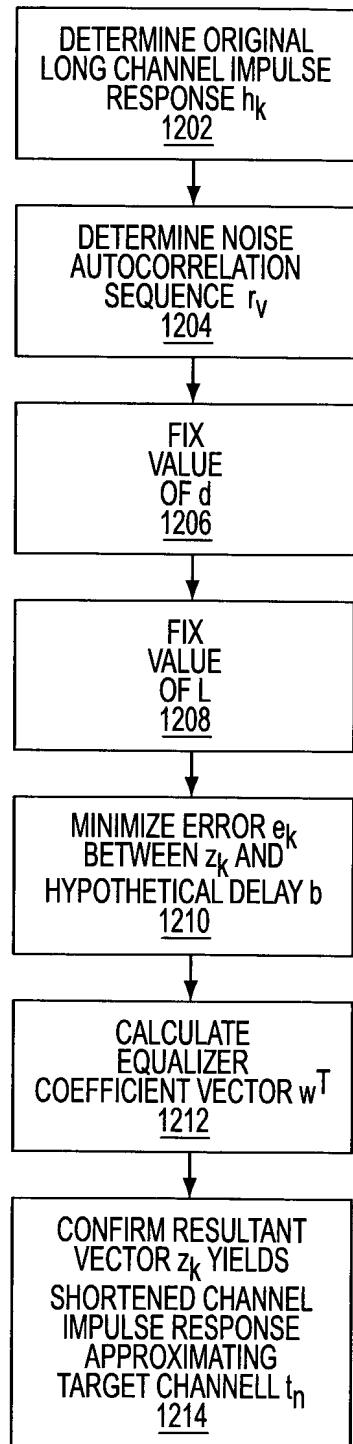


FIG. 12

**Method And System For Implementing A
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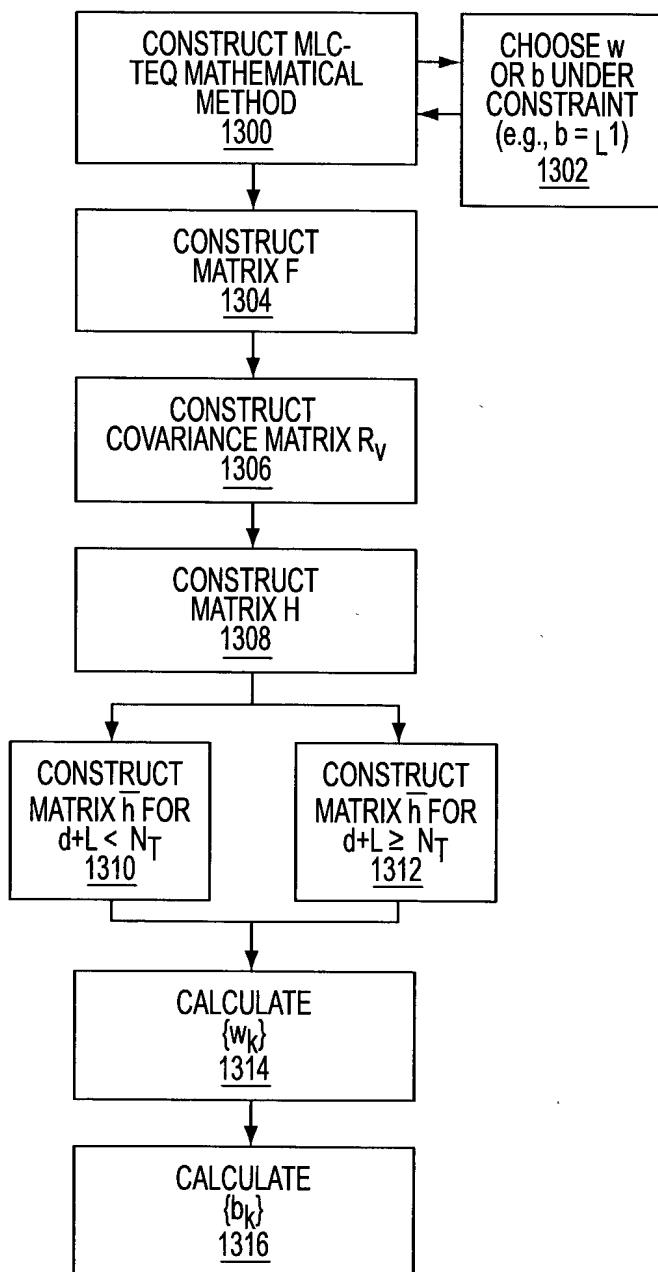


FIG. 13

“Method And System For Implementing A Reduced Complexity Dual Rate Echo Cancellation System”

“Canceller”
... Bülent Hadler et al.

Applicants: Bijit Hadler, et al.

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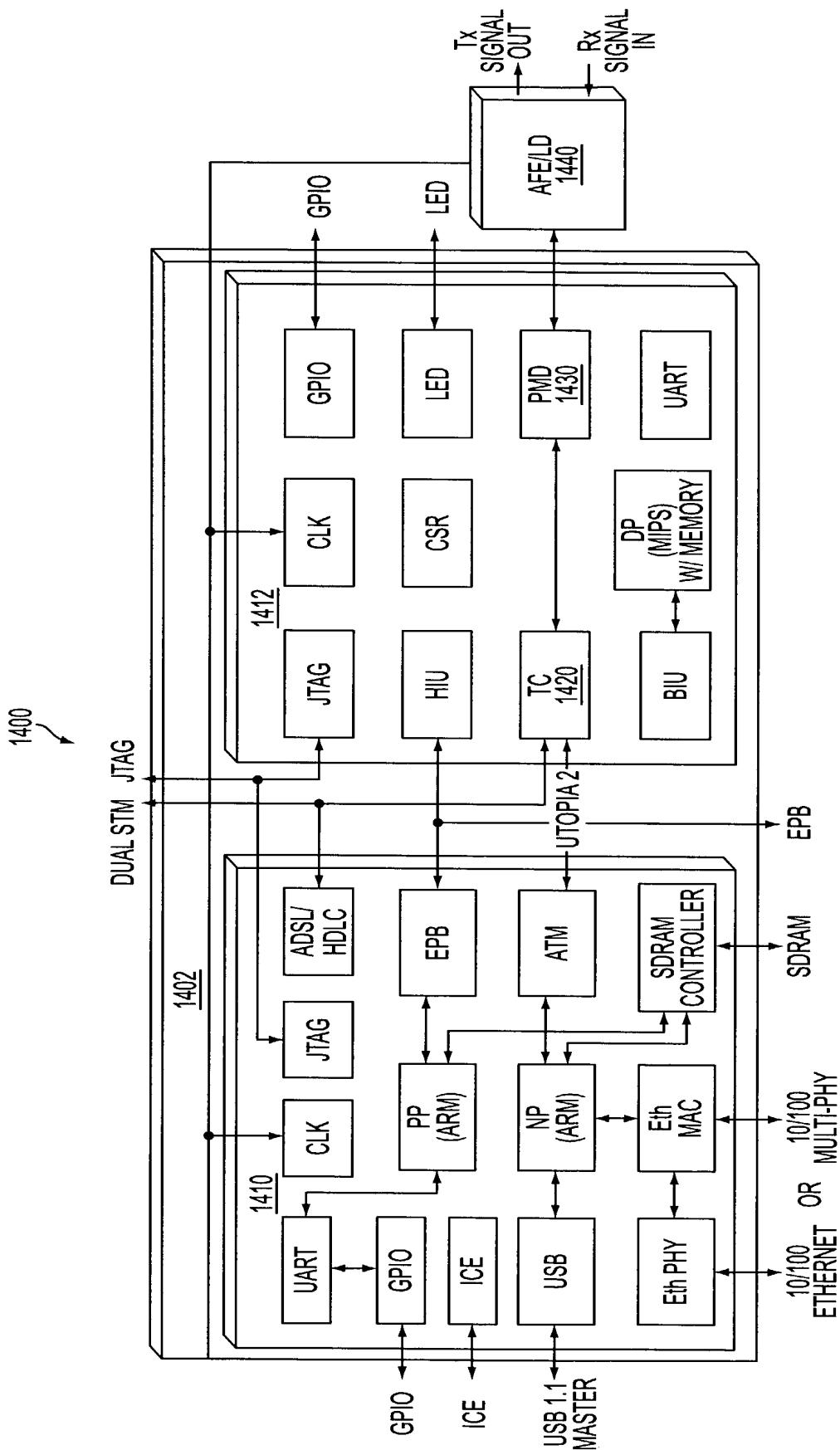


FIG. 14A

"Method And System For Implementing A
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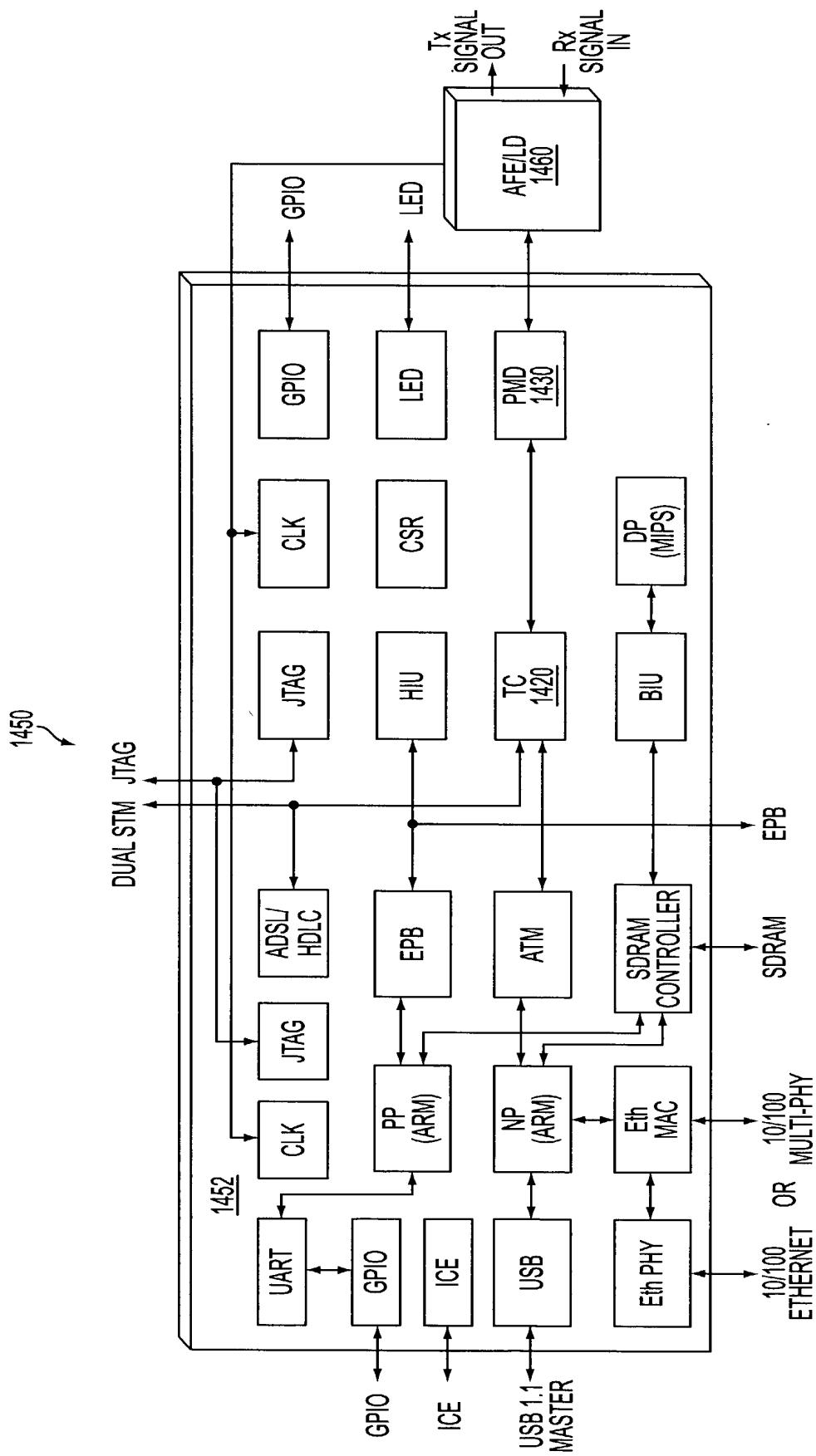


FIG. 14B

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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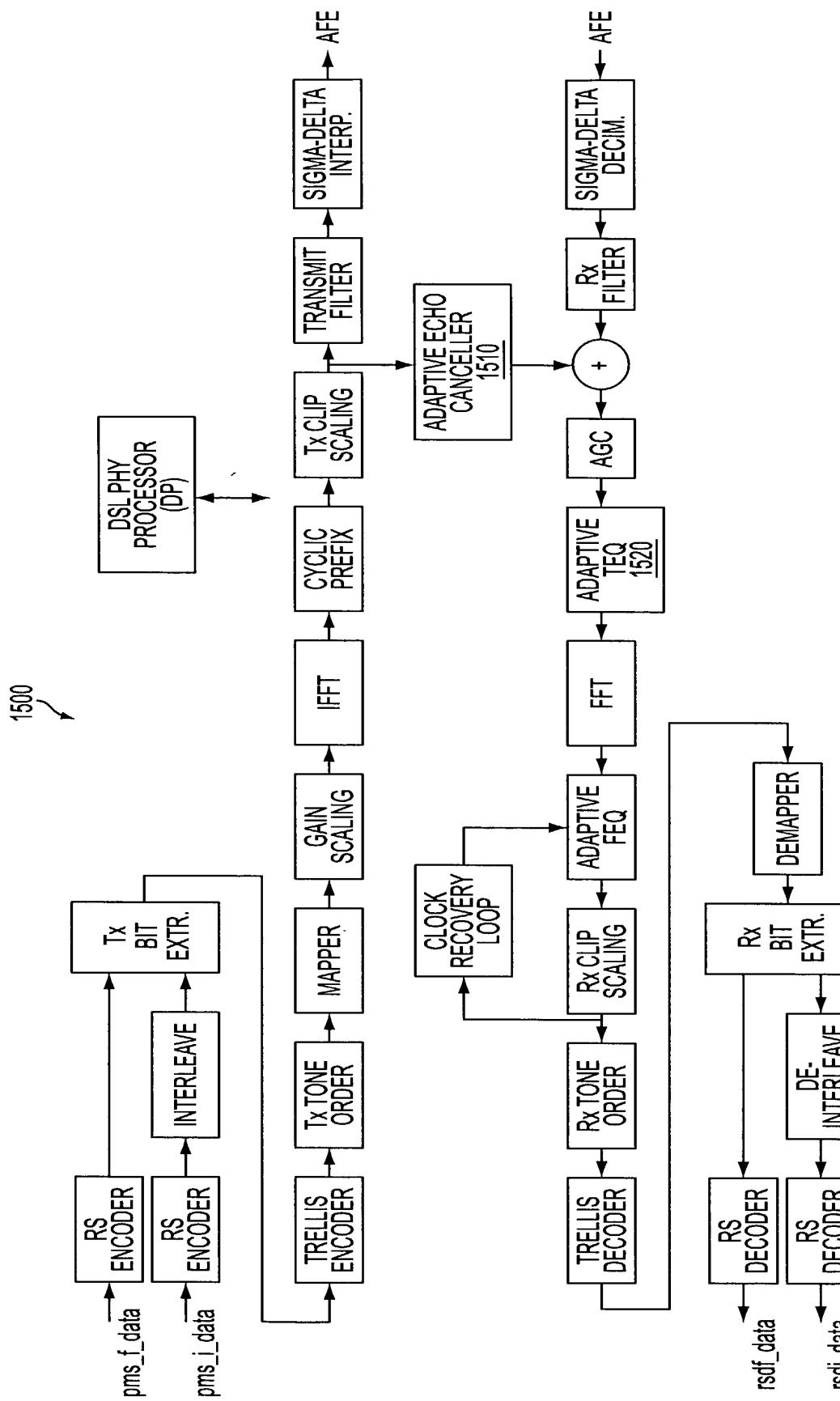


FIG. 15

"Method And System For Implementing A
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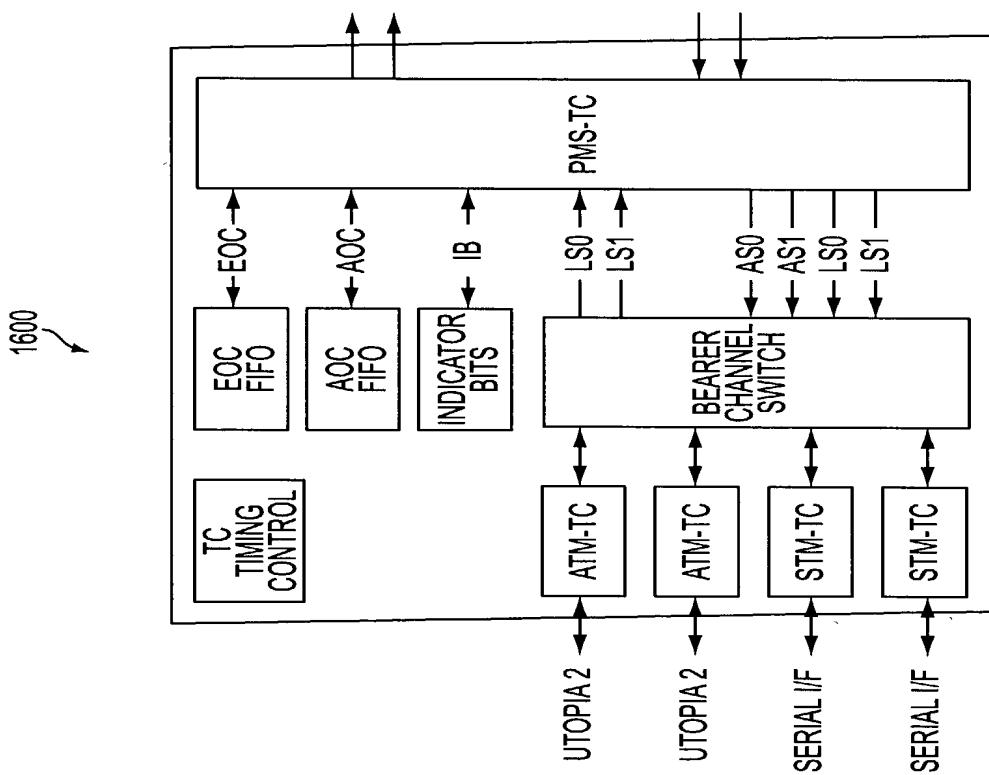


FIG. 16

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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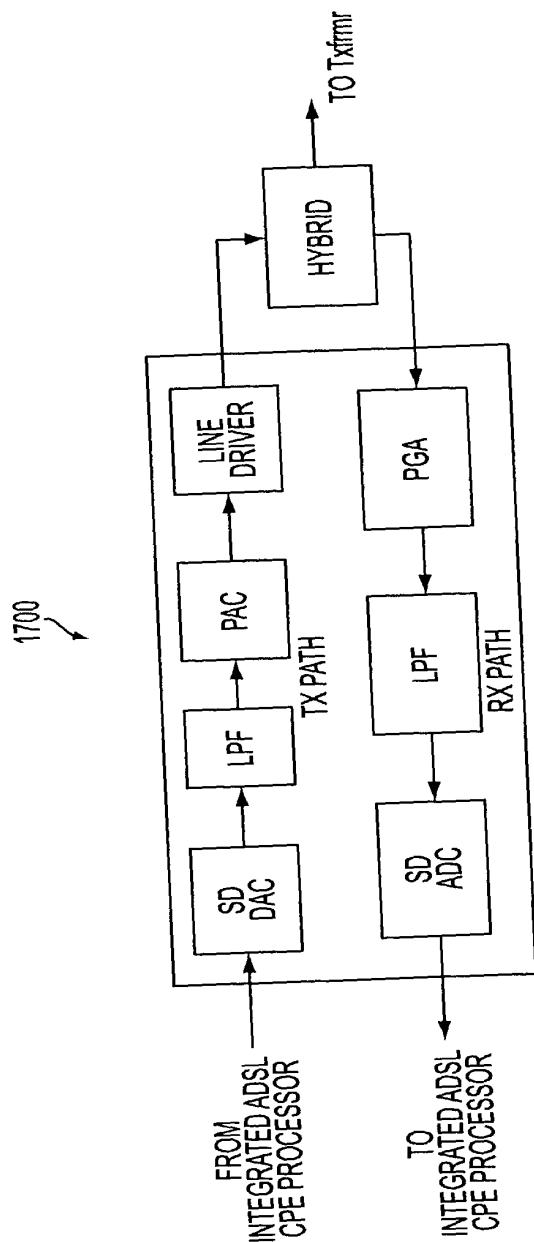


FIG. 17

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"
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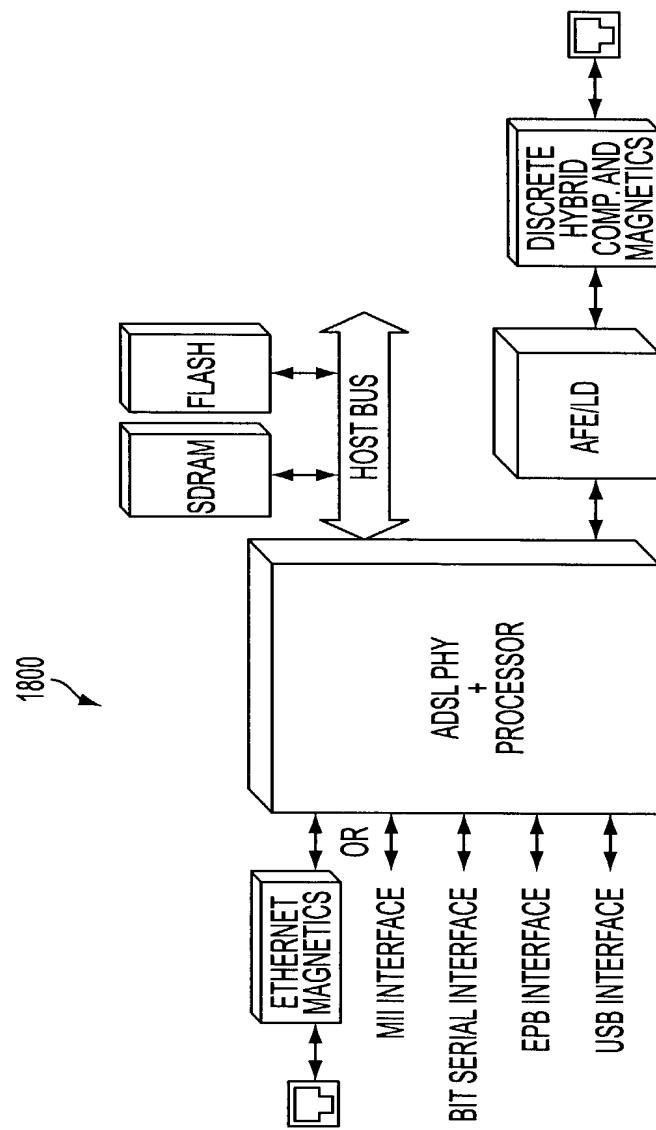


FIG. 18A

"Method And System For Implementing A
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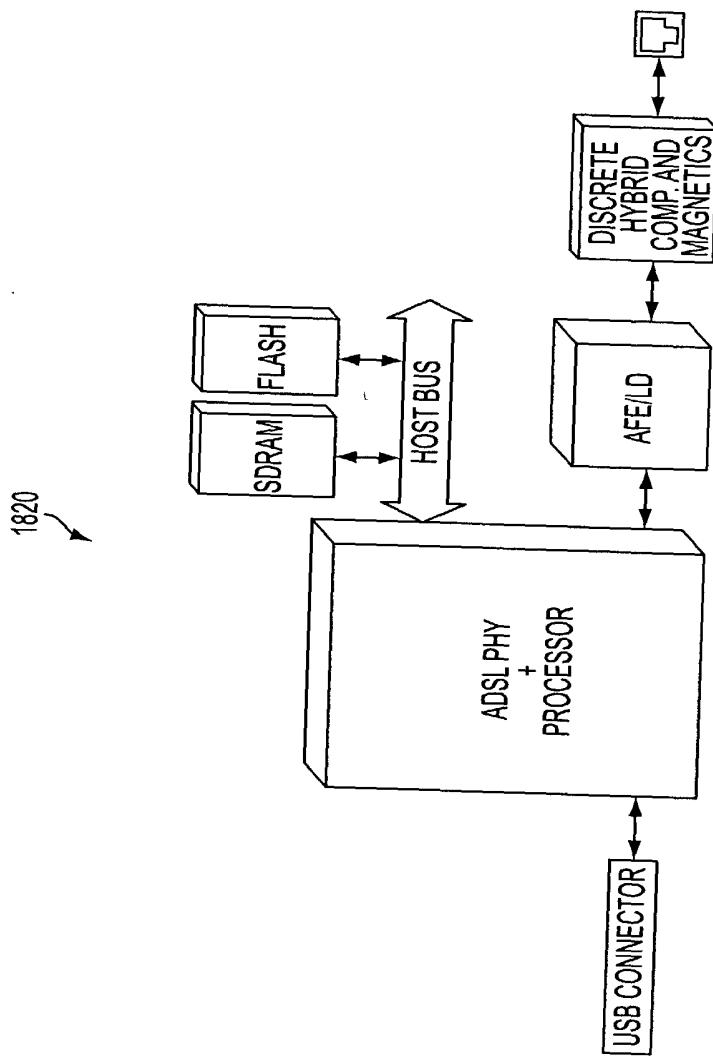


FIG. 18B

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"
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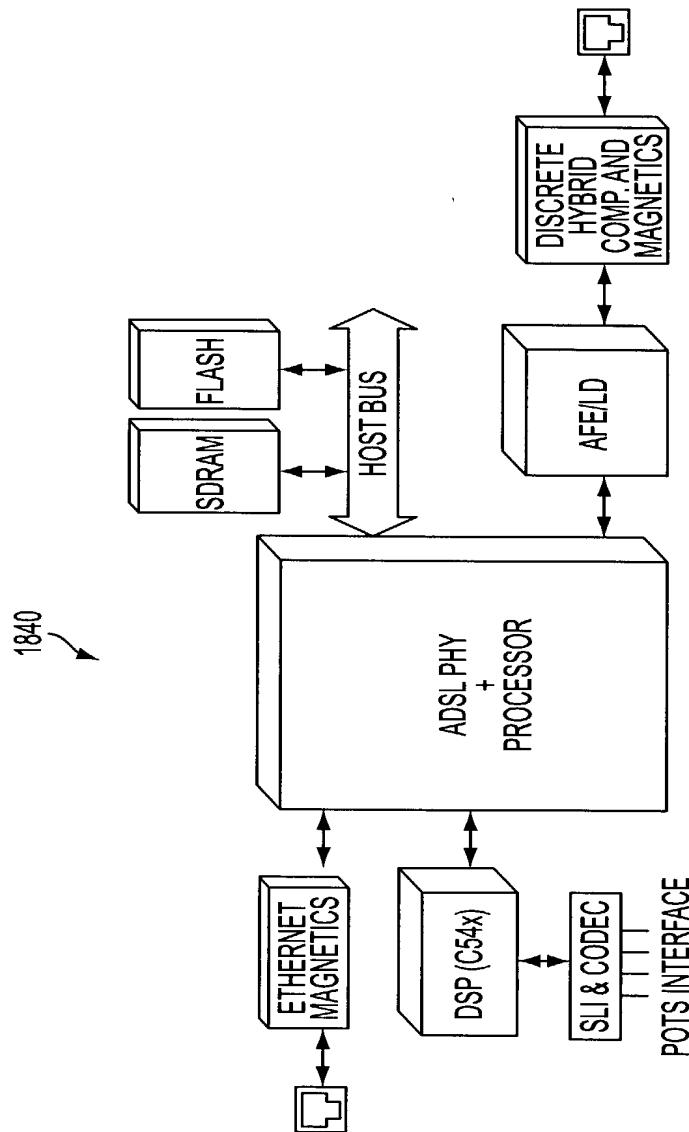


FIG. 18C